PTO-1449	Application No.	Applicant(s)
Information Disclosure Citation in an Application	Unknown	Fang Lu and Frank L. Wu

Docket Number	Group Art Unit	Filing Date
016295.0749	Unknown	February 28, 2002

U.S. PATENT DOCUMENTS

		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	A	5579522	11/26/96	Christeson et al.	395	652	07/24/95
	B	5634079	05/27/97	Buxton	395	892	05/15/92
	C	5870520	02/09/99	Lee et al.	395	182.04	03/13/96
	D	6003130	12/14/99	Anderson	713	2	10/28/96
	E	6018806	1/25/00	Cortopassi et al.	714	8	10/16/95
	F	6038663	03/14/00	Feldman	713	1	01/16/98
	G	6122733	09/19/00	Christeson et al.	713	2	09/30/97
	H	6161177	12/12/00	Anderson	713	2	10/6/97
	I	6167532	12/26/00	Wisecup	714	23	02/05/98

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO
	J							
	K							

NON-PATENT DOCUMENTS

		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
	L	IBM *POWER4 System Microarchitecture*, at internet <http://www-1.ibm.com/servers/eserver/pseries/hardware/whitepapers/power4_7.html> Pages 1-3	Printed 1/24/02
	M	IBM *Datacenter-class and HPC servers*, at internet site <http://www-1.ibm.com/servers/eserver/pseries/hardware/datactr/> Page 1	Printed 1/24/02
	N	PC Accelerate *Map to the Intel Chipset Maze: The 820 MCH-f*, at internet site <http://www.pcaccelerate.com/Chipsets/i820-1/i820-1.html > Pages 1-3	Printed 1/31/02
	O	Tony Smith, The Register *Intel details 16-way DDR-based I870 chipset*, at internet site <http://www.theregister.co.uk/content/archive/21258.html> Pages 1-2	Printed 2/27/02
	P	Anthony Cataldo, EE Times *Intel prepares for server chip set revival*, at internet site <http://www.eetimes.com/printableArticle?doc_id=OEG20010823S0066> Pages 1-3	Printed 2/27/02
	Q	U.S. Patent Application Serial No. 10/027,833 entitled *"System and Method for Updating Bios for a Multiple-Node Computer System,"* filed by Frank L. Wu et al.; Dell Products L.P. Assignee (DC-03185)	Filed 12/21/01

EXAMINER	DATE CONSIDERED
Paul F. Carter	8/10/04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

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